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## VU Answer

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Question No : 1 of 52 Marks: 1 (Budgeted Time 1 Min)

What does the instruction "ldr R3, 58" of SRC do?

Answer ( Please select your correct option )



- it will load the register R3 with the contents of the memory location M [PC+58]

  correct
- It will load the register R3 with the relative address itself (PC+58).
- It will store the register R3 contents to the memory location M [PC+58]
- No operation

Question No : 2 of 52 Marks: 1 (Budgeted Time 1 Min)

What functionality is performed by the instruction "lar R3, 36" of SRC?

Answer ( Please select your correct option )



- it will load the register R3 with the contents of the memory location M [PC+36]
- It will load the register R3 with the relative address itself (PC+36).

correct

- It will store the register R3 contents to the memory location M [PC+36]
- No operation

Question No : 3 of 52 Marks: 1 (Budgeted Time 1 Min)

What is the instruction length of the SRC processor?

Answer ( Please select your correct option )



- 8 bits
- 16 bits
- 32 bits

correct

- 64 bits

What does the word 'D' in the 'D-flip-Flop' stands for?



Answer ( Please select your correct option )

- Data **correct**
- Digital
- Dynamic
- Double

Almost every commercial computer has its own particular ---------- language



Answer ( Please select your correct option )

- assembly language

correct

- English language
- Higher level language
- 3GL

Which field of the machine language instruction is the "**type of operation**" that is to be performed?

Answer ( Please select your correct option )



- Op-code (or the operation code) **correct**
- CPU registers
- Memory cells
- I/O locations

_____ is/are defined as the time required processing a single instruction.



Answer ( Please select your correct option )

- Latency and Throughput
- Latency

correct

- Throughput
- Hazards

In pipelining ---------- is increased by overlapping the instruction execution



Answer ( Please select your correct option )

- Latency
- Throughput

correct

- Execution time
- Clock speed

Question No : 9 of 52 Marks: 1 (Budgeted Time 1 Min)

Which of the following register(s) takes input from the ALSU as the address of the memory location to be accessed and transfers the memory contents on that location onto the memory sub-system?

Answer ( Please select your correct option )



- Instruction Register
- Memory address register

correct

- Memory Buffer Register
- Registers A and C

Question No : 10 of 52 Marks: 1 (Budgeted Time 1 Min)

In which one of the following addressing modes, data is the part of the instruction itself, and so there is no need of address calculation?

Answer ( Please select your correct option )



- Direct Addressing Mode

correct

- Immediate addressing mode
- Indirect Addressing Mode
- Register (Direct) Addressing Mode

Question No : 11 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following type of error occurs when a 0 is received instead of a stop bit (which is always a 1)?

Answer ( Please select your correct option )



- Framing error

correct

- Parity error
- Over-run error
- Under-run error

Question No : 12 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is a technique in which some of the CPU's address lines forming an input to the address decoder are ignored.

Answer ( Please select your correct option )



- Partial decoding

correct

- Full encoding
- Partial multiplexing
- Half encoding

Question No : 13 of 52 Marks: 1 (Budgeted Time 1 Min)

Consider Falcon A, with 16 address lines, the total address space is _____ Kbytes.



Answer ( Please select your correct option )

- 2 ^ 16 **correct**
- 2 ^ 10
- 2 ^ 6
- 2 ^ 8

CPU can exchange data with a peripheral device using _____ technique.



Answer ( Please select your correct option )

- Memory Contention
- Direct Memory Access
- Pre-fetching
- Pipelining

correct

Which one of the following is **NOT** a technique used when the CPU wants to exchange data with peripheral device?



Answer ( Please select your correct option )

- Direct Memory Access
- Interrupt driven I/O
- Programmed I/O
- Virtual Memory

correct

Question No : 16 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one is the last instruction of the ISR that is to be executed when the ISR terminates?

Answer ( Please select your correct option )



- IRET **correct**
- IRQ
- INT
- NMI

Question No : 17 of 52 Marks: 1 (Budgeted Time 1 Min)

---------- is the time needed by the CPU to recognize (not service) an interrupt request.

Answer ( Please select your correct option )



- Interrupt Latency

  correct

- Response Deadline
- Timer delay
- Throughput

In Multiple Interrupt Lines approach, a number of interrupt lines are provided between the _____ modules.



Answer ( Please select your correct option )

- External and Internal
- CPU and I/O

correct

- CPU and Memory
- Memory and I/O

In FALCON-A assembler and simulator (FALSIM), variables are defined by using the _____ directive.



Answer ( Please select your correct option )

- .bin
- .equ

correct

- .iret
- .end

Question No : 20 of 52 Marks: 1 (Budgeted Time 1 Min)

In Direct memory access (DMA), a _____ is needed to control the total activity and to synchronize the transfer of data.

Answer ( Please select your correct option )



- DMA memory unit
- DMA controller

correct

- Control software
- Programmed I/O

Question No : 21 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ allows a peripheral device to read from and/or write to memory without intervention by the CPU.

Answer ( Please select your correct option )



- Programmed I/O
- Interrupt driven I/O
- Direct memory access

correct

- Polling

Question No : 22 of 52 Marks: 1 (Budgeted Time 1 Min)

A component connected to the system bus and having control of it during a particular bus cycle is called _____.

Answer ( Please select your correct option )



- Address decoder
- BIOS
- Master component

correct

- Slave component

A Hard Disk sector has the _____ parts.



Answer ( Please select your correct option )

- Header only
- Data section and a trailer
- Data section only
- Header, data section and a trailer

correct

Question No : 24 of 52 Marks: 1 (Budgeted Time 1 Min)

CRC has _____overhead as compared to Hamming code.



Answer ( Please select your correct option )

- Equal
- Greater
- Lesser

correct

- Absolutely no

_____ are computed by the ALU and stored in processor status register.



Answer ( Please select your correct option )

- Condition Codes

correct

- Control Signals
- Flip Flops
- Multiplexers

Question No : 26 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ occurs when the exponent is too large and can not be represented in the exponent field.

Answer ( Please select your correct option )



- Underflow
- Overflow

correct

- Rounding Off
- Normalization

Question No : 27 of 52 Marks: 1 (Budgeted Time 1 Min)

In Single-Precision Binary Floating Point Representation the size of exponent is _____.

Answer ( Please select your correct option )



- 8-bits **correct**
- 11-bits
- 1-bits
- 23-bits

The _____ is m-bits wide and contains memory address generated by the CPU directly connected to the m-bit wide address bus.



Answer ( Please select your correct option )

- Memory address register (MAR) **correct**
- Program counter register
- Accumulator register
- Instruction register

_____ is a place for safe storage and provides the fastest possible storage after the registers.



Answer ( Please select your correct option )

- Hard Disk
- Cache

correct

- Compact Disk
- Floppy Disk

Question No : 30 of 52 Marks: 1 (Budgeted Time 1 Min)

For a request for data, if the data is available in the cache it results in a _____.

Answer ( Please select your correct option )



- Cache Miss
- Spatial Locality
- Temporal Locality
- Cache Hit

correct

Question No : 31 of 52 Marks: 1 (Budgeted Time 1 Min)

For write to complete in Write through, the CPU has to wait. This wait state is called _____.

Answer ( Please select your correct option )



- Write Through
- Write Back
- Write Allocate
- Write Stall

correct

_____ contains permanent pattern of data that cannot be changed.

Answer ( Please select your correct option )



- RAM
- Hard Disk
- Cache
- ROM

correct

Question No : 33 of 52 Marks: 1 (Budgeted Time 1 Min)

In Control Field of page table, _____ indicate the availability of page in main memory.



Answer ( Please select your correct option )

- Access Control Bits
- Used Bits
- Presence Bits

correct

- Redundant Bits

In _____technique memory is divided into segments of variable sizes depending upon the requirements.



Answer ( Please select your correct option )

- Multiplexing
- Segmentation

correct

- Hamming code
- Partial decoding

_____ depends upon the average number of calls and the service time taken by a particular server.



Answer ( Please select your correct option )

- Throughput

correct

- Latency
- Poisson Distribution
- Response Time

Question No : 36 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is also called traffic intensity and its value must be between 0 and 1.

Answer ( Please select your correct option )



- Little's Law
- Poisson Distribution
- Server Utilization

correct

- SPEC

Question No : 37 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is the maximum rate at which data can be transmitted through networks.

Answer ( Please select your correct option )



- Transmission Time
- Latency
- Transport Latency
- Bandwidth

correct

Question No : 38 of 52 Marks: 1 (Budgeted Time 1 Min)

The time for the message to pass through the network, except the time of flight is called _____.

Answer ( Please select your correct option )



- Transmission Time — correct
- Latency
- Transport Latency
- Bandwidth

Question No : 39 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ refers to the interconnection of machines in a building or a campus.

Answer ( Please select your correct option )



- SAN
- LAN

correct

- WAN
- MAN

SPARC (Scalable Processor Architecture) is an example of _____architecture.



Answer ( Please select your correct option )

- CISC
- RISC

correct

- SRC
- FALCON

Question No : 1 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following is the memory organization of **EAGLE processor?**



Answer ( Please select your correct option )

- $2^8$ * 8 bits
- $2^{16}$ * 8 bits



- $2^{32}$ * 8 bits
- $2^{64}$ * 8 bits

Question No : 2 of 52 Marks: 1 (Budgeted Time 1 Min)

----------is a collection of binary digits or bits that the computer reads and interprets.



Answer ( Please select your correct option )

- assembly language
- higher level language
- English language
- machine language

Question No : 3 of 52 Marks: 1 (Budgeted Time 1 Min)

In which one of the following addressing modes, the value to be stored in memory is obtained by directly retrieving it from another memory location?



Answer ( Please select your correct option )

- Direct Addressing Mode
- Immediate addressing mode
- Indirect Addressing Mode
- Register (Direct) Addressing Mode

Question No : 4 of 52 Marks: 1 (Budgeted Time 1 Min)

The external interface of FALCON-A consists of a _____address bus and a _____ data bus.



Answer ( Please select your correct option )

- 8-bit , 8-bit
- 16-bit , 16-bit
- 16-bit , 24-bit
- 16-bit , 32-bit

Question No : 5 of 52 Marks: 1 (Budgeted Time 1 Min)

Execution time of a program with respect to the processor is calculated as:



Answer ( Please select your correct option )

- Execution Time = IC x CPI x MIPS
- Execution Time = IC x CPI x T

  ans
- Execution Time = CPI x T x MFLOPS
- Execution Time = IC x T

Question No : 6 of 52 Marks: 1 (Budgeted Time 1 Min)

In which of the following instructions the data move between a register in the processor and a memory location (or another register) and are also called data movement?



Answer ( Please select your correct option )

- Arithmetic
- Load/store

ans

- Control
- Logic

Question No : 7 of 52 Marks: 1 (Budgeted Time 1 Min)

The _____ instruction is completed once memory access has been made and the memory location has been written to.



Answer ( Please select your correct option )

- Store

p 208

- Branch
- Load
- Control

Question No : 8 of 52 Marks: 1 (Budgeted Time 1 Min)

Which type of instructions enables mathematical computations?



Answer ( Please select your correct option )

- Arithmetic

page 92

- Control
- Data transfer
- Miscellaneous

Question No : 9 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ control signal allows the bus to read from the selected register.



Answer ( Please select your correct option )

- RBE
- RCE
- LCON
- R2BUS

Question No : 10 of 52 Marks: 1 (Budgeted Time 1 Min)

For any of the instructions that are a part of the instruction set of the SRC, there are certain _____ required; which may be used to select the appropriate function for the ALU to b performed, to select the appropriate registers, or the appropriate memory location.



Answer ( Please select your correct option )

- Registers
- Control signals p 171 ans
- Memory
- DMA controllers

Question No : 11 of 52 Marks: 1 (Budgeted Time 1 Min)

Every I/O port has a unique identifier associated with it, which is called its ----------



Answer ( Please select your correct option )

- Address

  ans
- Access point
- Interval Identifier
- Device driver

Question No : 12 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is a technique in which some of the CPU's address lines forming an input to the address decoder are ignored.



Answer ( Please select your correct option )

- Partial decoding

  ans

- Full encoding
- Partial multiplexing
- Half encoding

Question No : 13 of 52 Marks: 1 (Budgeted Time 1 Min)

Partial decoding is an attractive choice in _____.



Answer ( Please select your correct option )

- Small systems

  ans
- Large systems
- Medium systems
- All of the above

Question No : 14 of 52 Marks: 1 (Budgeted Time 1 Min)

Consider Falcon A, with 16 address lines, the total address space is _____ Kbytes.



Answer ( Please select your correct option )

- 2 ^ 16
  ans
- 2 ^ 10
- 2 ^ 6
- 2 ^ 8

Question No : 15 of 52 Marks: 1 (Budgeted Time 1 Min)

In which one of the following methods, does the CPU poll to identify the interrupting module and branch to an interrupt service routine on detecting an interrupt?



Answer ( Please select your correct option )

- Multiple interrupt lines
- Software Poll

ans

- Daisy Chain
- Parallel Priority

Question No : 16 of 52 Marks: 1 (Budgeted Time 1 Min)

Maskable Interrupts are applied to the _____ pin of the processor.



Answer ( Please select your correct option )

- INTR

  ans
- NMI
- IRET
- INT

Question No : 17 of 52 Marks: 1 (Budgeted Time 1 Min)

Non-maskable Interrupts are detected using the _____ pin of the processor.



Answer ( Please select your correct option )

- INTR
- NMI
  ans
- IRET
- INT

Question No : 18 of 52 Marks: 1 (Budgeted Time 1 Min)

In Multiple Interrupt Lines approach, a number of interrupt lines are provided between the _____ modules.



Answer ( Please select your correct option )

- External and Internal
- CPU and I/O

ans

- CPU and Memory
- Memory and I/O

Question No : 19 of 52 Marks: 1 (Budgeted Time 1 Min)

In FALCON-A assembler and simulator (FALSIM), variables are defined by using the _____ directive.



Answer ( Please select your correct option )

- .bin
- .equ
- .iret
- .end

Question No : 20 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ allows a peripheral device to read from and/or write to memory without intervention by the CPU.



Answer ( Please select your correct option )

- Programmed I/O
- Interrupt driven I/O
- Direct memory access

ans

- Polling

Question No : 21 of 52 Marks: 1 (Budgeted Time 1 Min)

Taking control of the system bus for a few bus cycles is known as _____.



Answer ( Please select your correct option )

- Bus Scheduling
- Cycle Stealing
- Cycle Transferring
- CPU Scheduling

ans

Question No : 22 of 52 Marks: 1 (Budgeted Time 1 Min)

A component connected to the system bus and having control of it during a particular bus cycle is called _____.



Answer ( Please select your correct option )

- Address decoder
- BIOS
- Master component

  ans
- Slave component

Question No : 23 of 52 Marks: 1 (Budgeted Time 1 Min)

When it is required to read data from a particular location of the disk, the head moves towards the selected track and this process is called_____.



Answer ( Please select your correct option )

- Fragmentation
- Defragmentation
- Seek

  ans
- Encoding

Question No : 24 of 52 Marks: 1 (Budgeted Time 1 Min)

CRC has _____overhead as compared to Hamming code.



Answer ( Please select your correct option )

- Equal
- Greater
- Lesser ans
- Absolutely no

Question No : 25 of 52 Marks: 1 (Budgeted Time 1 Min)

The conversion of numbers from a representation in one base to another is known as_____



Answer ( Please select your correct option )

- Radix Conversion

  ans
- Number Representation
- Decimal Representation
- Hexadecimal Representation

Question No : 26 of 52 Marks: 1 (Budgeted Time 1 Min)

In _____, bits are encoded in pairs so there are only ' n/2' additions instead of 'n'.



Answer ( Please select your correct option )

- Booth Recoding
- Bit-Pair Recoding

ans

- Fraction Division
- Integer Division

Question No : 27 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ are computed by the ALU and stored in processor status register.



Answer ( Please select your correct option )

- Condition Codes

ans

- Control Signals
- Flip Flops
- Multiplexers

Question No : 28 of 52 Marks: 1 (Budgeted Time 1 Min)

In computers, floating-point representation uses _____to encode significand, exponent and their sign in a single word.



Answer ( Please select your correct option )

- Decimal Numbers
- Binary Numbers

ans

- Octal Numbers
- Hexadecimal Numbers

Question No : 29 of 52 Marks: 1 (Budgeted Time 1 Min)

In floating point representations _____ is also called mantissa.



Answer ( Please select your correct option )

- Sign
- Base
- Significand

ans p 347

- Exponent

Question No : 30 of 52 Marks: 1 (Budgeted Time 1 Min)

In Single-Precision Binary Floating Point Representation the size of exponent is _____.



Answer ( Please select your correct option )

- 8-bits

ans

- 11-bits
- 1-bits
- 23-bits

Question No : 31 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is nonvolatile i.e. it retains the information in it when power is removed from it.



Answer ( Please select your correct option )

- RAM
- DRAM
- ROM

ans

- SRAM

Question No : 32 of 52 Marks: 1 (Budgeted Time 1 Min)

Based on the statistical results, the cache block which has been least used in the recent past, is replaced with a new block. This technique is called _____.



Answer ( Please select your correct option )

- Always Replacement
- Random Replacement
- LFU (Least Frequently Used)
  ans
- Write Allocate

Question No : 33 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ acts as a cache between main memory and secondary memory.



Answer ( Please select your correct option )

- Read Only Memory
- Flash Memory
- Virtual Memory

ans

- Magnetic Tape

Question No : 34 of 52 Marks: 1 (Budgeted Time 1 Min)

Randomly replacing any older page to bring in the desired page is known as _____.



Answer ( Please select your correct option )

- Always Replacement
- LFU (Least Frequently Used)
- Random Replacement

ans

- Fragmentation

Question No : 35 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ are formed by concatenating the page number with the word number.



Answer ( Please select your correct option )

- Memory chips
- Protocols
- Hazards
- Virtual addresses

ans

Question No : 36 of 52 Marks: 1 (Budgeted Time 1 Min)

In _____technique memory is divided into segments of variable sizes depending upon the requirements.



Answer ( Please select your correct option )

- Multiplexing
- Segmentation

ans

- Hamming code
- Partial decoding

Question No : 37 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ depends upon the average number of calls and the service time taken by a particular server.



Answer ( Please select your correct option )

- Throughput

  ans
- Latency
- Poisson Distribution
- Response Time

Question No : 38 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is also called traffic intensity and its value must be between 0 and 1.



Answer ( Please select your correct option )

- Little's Law
- Poisson Distribution
- Server Utilization

ans

- SPEC

Question No : 39 of 52 Marks: 1 (Budgeted Time 1 Min)

The time for the message to pass through the network, except the time of flight is called _____.



Answer ( Please select your correct option )

- Transmission Time ans
- Latency
- Transport Latency
- Bandwidth

Question No : 40 of 52 Marks: 1 (Budgeted Time 1 Min)

A set of rules followed by different components in a network is called _____.



Answer ( Please select your correct option )

- Host
- Connectivity
- Resource Sharing
- Protocol

ans

Question No : 1 of 52 Marks: 1 (Budgeted Time 1 Min)

What is the size of the memory space that is available to **SRC processor?**

Answer ( Please select your correct option )



- $2^8$ bytes
- $2^{16}$ bytes
- $2^{32}$ bytes

correct

- $2^{64}$ bytes

Question No : 2 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following is the memory organization of **SRC processor?**

Answer ( Please select your correct option )



- $2^8$ * 8 bits
- $2^{16}$ * 8 bits
- $2^{32}$ * 8 bits

correct

- $2^{64}$ * 8 bits

Question No : 3 of 52 Marks: 1 (Budgeted Time 1 Min)

In which one of the following addressing modes, the value to be stored in memory is obtained by directly retrieving it from another memory location?

Answer ( Please select your correct option )



- Direct Addressing Mode — correct
- Immediate addressing mode
- Indirect Addressing Mode
- Register (Direct) Addressing Mode

Which operator is used to 'name' registers, or part of registers, in the Register Transfer Language?



Answer ( Please select your correct option )

- := correct
- &
- %
- ©

Question No : 5 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following register holds the address of the next instruction to be executed?

Answer ( Please select your correct option )



- Accumulator
- Address Mask
- Instruction Register
- Program Counter

correct

Question No : 6 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following register holds the instruction that is being executed?

Answer ( Please select your correct option )



- Accumulator
- Address Mask
- Instruction Register
- Program Counter

correct

Which one of the following is the highest level of abstraction in digital design in which the computer architect views the system for the description of system components and their interconnections?



Answer ( Please select your correct option )

- Processor-Memory-Switch level (PMS level)

correct

- Instruction Set Level
- Register Transfer Level
- Logic design level

Which one of the following design levels is called the gate level?



Answer ( Please select your correct option )

- Logic Design Level

correct

- Circuit Level
- Mask Level
- Register transfer Level

Question No : 9 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ Instructions usually involve calculating the target address and evaluating a condition.

Answer ( Please select your correct option )



- Add
- Branch
- Load
- Store

correct

Question No : 10 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following instructions is used to load register from memory using a relative address?



Answer ( Please select your correct option )

- la
- nop
- ldr

correct

- str

Question No : 11 of 52 Marks: 1 (Budgeted Time 1 Min)

An interface that is used to connect the computer bus with I/O devices is called _____.

Answer ( Please select your correct option )



- Buffer
- I/O port

correct

- Memory mapping
- Processor

Question No : 12 of 52 Marks: 1 (Budgeted Time 1 Min)

Every I/O port has a unique identifier associated with it, which is called its ----------



Answer ( Please select your correct option )

- Address

correct

- Access point
- Interval Identifier
- Device driver

Question No : 13 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ refers to the situation in which all I/O operations are performed under the direct control of a program running on the CPU.

Answer ( Please select your correct option )



- Direct memory access
- Virtual memory
- Partial decoding
- Programmed I/O

correct

Question No : 14 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is the process of periodically checking the status of a device to see if it is ready for the next I/O operation.

Answer ( Please select your correct option )



- Polling

correct

- Snooping
- Data Bus Multiplexing
- Pipelining

Question No : 15 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one of the following is **NOT** a technique used when the CPU wants to exchange data with peripheral device?

Answer ( Please select your correct option )



- Direct Memory Access
- Interrupt driven I/O
- Programmed I/O
- Virtual Memory

correct

Question No : 16 of 52 Marks: 1 (Budgeted Time 1 Min)

Which one is the last instruction of the ISR that is to be executed when the ISR terminates?

Answer ( Please select your correct option )



- IRET

correct

- IRQ
- INT
- NMI

Question No : 17 of 52 Marks: 1 (Budgeted Time 1 Min)

---------- is the time needed by the CPU to recognize (not service) an interrupt request.

Answer ( Please select your correct option )



- Interrupt Latency

correct

- Response Deadline
- Timer delay
- Throughput

Question No : 18 of 52 Marks: 1 (Budgeted Time 1 Min)

In Multiple Interrupt Lines approach, a number of interrupt lines are provided between the _____ modules.

Answer ( Please select your correct option )



- External and Internal
- CPU and I/O

correct

- CPU and Memory
- Memory and I/O

Falcon-A Simulator loads a FALCON-A binary file with a _____ extension and presents its contents into different areas of the simulator.

Answer ( Please select your correct option )



- .bin
- .binfa

correct

- .fa
- .asmfa

In Direct memory access (DMA), a _____ is needed to control the total activity and to synchronize the transfer of data.

Answer ( Please select your correct option )



- DMA memory unit
- DMA controller

correct

- Control software
- Programmed I/O

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Answer ( Please select your correct option )



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- Interrupt driven I/O
- Direct memory access

correct

- Polling

Taking control of the system bus for a few bus cycles is known as _____.

Answer ( Please select your correct option )



- Bus Scheduling
- Cycle Stealing

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- Cycle Transferring
- CPU Scheduling

A component connected to the system bus and having control of it during a particular bus cycle is called _____.

Answer ( Please select your correct option )



- Address decoder
- BIOS
- Master component

correct

- Slave component

CRC has _____overhead as compared to Hamming code.



Answer ( Please select your correct option )

- Equal
- Greater
- Lesser

correct

- Absolutely no

We represent e^ instead of e to show_____.



Answer ( Please select your correct option )

- Sign Magnitude Form
- Radix Complement Form
- Diminished Radix Compliment Form
- Biased Representation

correct

In floating point representations _____ is also called mantissa.

Answer ( Please select your correct option )



- Sign
- Base
- Significand
- Exponent

correct

_____ occurs when the exponent is too large and can not be represented in the exponent field.

Answer ( Please select your correct option )



- Underflow
- Overflow

correct

- Rounding Off
- Normalization

In Double-Precision Binary Floating Point Representation the size of fraction is _____.

Answer ( Please select your correct option )



- 23-bits
- 52-bits

correct

- 11-bits
- 1-bits

Question No : 29 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is a combination of arithmetic, logic and shifter unit along with some multiplexers.

Answer ( Please select your correct option )



- Computer Bus
- CPU Register
- Flip Flop
- ALU

correct

Question No : 30 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is a place for safe storage and provides the fastest possible storage after the registers.

Answer ( Please select your correct option )



- Hard Disk
- Cache

correct

- Compact Disk
- Floppy Disk

Question No : 31 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is nonvolatile i.e. it retains the information in it when power is removed from it.

Answer ( Please select your correct option )



- RAM
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- ROM

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- SRAM

Question No : 32 of 52 Marks: 1 (Budgeted Time 1 Min)

Based on the statistical results, the cache block which has been least used in the recent past, is replaced with a new block. This technique is called _____.



Answer ( Please select your correct option )

- Always Replacement
- Random Replacement
- LFU (Least Frequently Used)

correct

- Write Allocate

Question No : 33 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ acts as a cache between main memory and secondary memory.

Answer ( Please select your correct option )



- Read Only Memory
- Flash Memory
- Virtual Memory

correct

- Magnetic Tape

In _____technique memory is divided into segments of variable sizes depending upon the requirements.



Answer ( Please select your correct option )

- Multiplexing
- Segmentation

correct

- Hamming code
- Partial decoding

Question No : 35 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ is the maximum rate at which data can be transmitted through networks.

Answer ( Please select your correct option )



- Transmission Time
- Latency
- Transport Latency
- Bandwidth

correct

In physical media of networks, for increased and better performance we use _____ which are usually made of glass.

Answer ( Please select your correct option )



- Coaxial Cables
- Twisted Pair Cables
- Fiber Optic Cables

correct

- Shielded Twisted Pair Cables

In _____ topology, all the computers are connected in the form of a circle.



Answer ( Please select your correct option )

- Bus
- Ring
- Mesh
- Star

correct

Connection Oriented Communication reserves the _____ until the transfer is completed.



Answer ( Please select your correct option )

- Bandwidth

correct

- Error
- Checksum
- Protocol

Question No : 39 of 52 Marks: 1 (Budgeted Time 1 Min)

In connection-less communication message is divided into _____.

Answer ( Please select your correct option )



- Tracks
- Sectors
- Platters
- Packets

correct

SPARC (Scalable Processor Architecture) is an example of _____architecture.



Answer ( Please select your correct option )

- CISC
- RISC

correct

- SRC
- FALCON